

ABSTRACT

A system and method is presented for simultaneous processing of information by providing a matrix having numerous cells, each of which has at least one processor and pages having a combination of format counters, data pointers and process counters. The cells may be connected to an area of random access memory, which is addressable, with the connection being dynamically re-allocable to other areas of random access memory. Instructions and registers are located in random access memory areas. The cells can process information addressable over one or more networks, including without limitation the Internet, executing instructions pointed to by at least one process counter.

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